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MERL-1559

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:  
Perry, et al.

:  
: Art Unit: 2676

Serial No: 10/802,468

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:  
: Examiner: D. Singh

Filed: March 16, 2004

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:  
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Title: PIPELINE AND CACHE FOR PROCESSING DATA PROGRESSIVELY

Date of Deposit:

October 18, 2005

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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
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Pursuant to 37 C.F.R. §1.56(a), Applicant hereby cites the following documents (copies enclosed) listed on the attached copy of Form PTO-1449.

This Information Disclosure Statement is filed in accordance with the paragraph of 37 CFR §1.97 checked below:

     1.97(b) This Information Disclosure Statement is filed:

- (1) Within three months of the filing date of a national application; OR
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- (3) Before the mailing of a first Office Action on the merits.

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  X   1.97(c) This Information Disclosure Statement is filed after the period specified in paragraph (b) above, but before the mailing date of either:

- (1) A Final Action under 37 CFR 1.113; OR
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  X   the Certification under 37 CFR  
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       the fee of \$240.00 under 37 CFR  
1.17(p).

       1.97(d) This Information Disclosure Statement is filed  
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- (1) the Certification under 37 CFR 1.97(e) as  
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If this Information Disclosure Statement is being filed under 37 CFR 1.97(c) or 1.97(d), the undersigned Attorney hereby certifies that:

X each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing date of this Statement;

or

       no item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, or to the knowledge of the undersigned Attorney after making reasonable enquiry, was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing date of this Statement.

MERL-1559

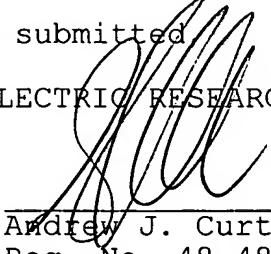
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Respectfully submitted

MITSUBISHI ELECTRIC RESEARCH LABORATORIES

By:

  
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Andrew J. Curtin  
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Attorney for Assignee

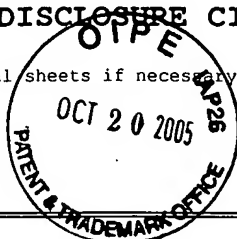
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Enclosures

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Form PTO-1449 (modified 2/91)	U.S. DEPT OF COMMERCE Patent and Trademark Office	Attorney Docket Number: MERL-1559	Serial Number: 10/802,468
<b>INFORMATION DISCLOSURE CITATION</b> (Use several sheets if necessary)		Applicant: Perry et al.	
		Filing date: March 16, 2004	Group art area: 2676



## U.S. PATENT DOCUMENTS

Examiner Initial	Patent number	Date	Name	Class	Sub-class	Filing date if appropriate
	US2003/0067468 A1	10 Apr 2003	Duluk, JR et al.	345	506	
	US2004/0189653 A1	30 Sept 2004	Perry et al.	345	557	

## FOREIGN PATENT DOCUMENTS

	Document number	Date	Country	Class	Subclass	Translation	
						YES	NO
	WO03/081445 A1	2 Oct 2003	WIPO	G06F	15/00		X

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

1.	Taylor, M. B. et al. "Scalar Operand Networks: On-Chip Interconnect for ILP in Partitioned Architectures" High-Performance Computer Architecture, 2003. 8-12 Feb 2003, pp. 341-353. XP-010629526.
2.	Scott, N. D. et al. "An Overview of the VISUALIZE fx Graphics Accelerator Hardware" Hewlett-Packard Journal, vol. 49, no. 2, pp. 28-34, May 1998. XP-000865343.
3.	
4.	
5.	

Examiner:	Date Considered:
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP .609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.